

# SANYO Semiconductors DATA SHEET

## LA73051

## Monolithic Linear IC

## 3ch 75 $\Omega$ Video Driver

#### Overview

This LA73051 is a 3ch  $75\Omega$  Video Driver IC. The LA73051 is ideal for use the video output driver such as VCR and DVD-player equipment.

#### **Functions**

WWW

• 6dB AMP+driver (3ch)

#### **Specifications**

#### **Maximum Ratings** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		±7, +14	V
Allowable power dissipation	Pd max	Ta ≤ 80°C *	440	mW
Operating temperature	Topr		-20 to +80	°C
Storage temperature	Tstg		-55 to +150	°C

<sup>\*:</sup> Mounted on a board : 114.3×76.1×1.6mm³ glass epoxy board.

#### **Recommended Operating Conditions** at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Recommending operation voltage	Vcс		±5	V
			+9	
Operating voltage range	V <sub>CC</sub> op		±4.0 to ±5.5	V
			+8 to +10	

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#### LA73051

## Electrical Characteristics at $Ta=25^{\circ}C,\ V_{CC}=\pm5V$

Danamatan	Oh. al	One distance		Ratings		1.1-24
Parameter	Symbol	Conditions	min	typ	max	Unit
Current dissipation	I <sub>CC</sub> 1	No signal	28.9	34.0	39.1	mA
Voltage gain	VG	V <sub>IN</sub> = 1Vp-p, f = 4.43MHz	5.7	6.2	6.7	dB
Frequency characteristics 1	VF1	V <sub>IN</sub> = 1Vp-p, f = 100k/5MHz	-1.0	0	1.0	dB
Frequency characteristics 2	VF2	V <sub>IN</sub> = 1Vp-p, f = 100k/27MHz		-25	-20	dB
Group delay	GD	f = 100k/4.43MHz		±10	±15	ns
Maximum output level	V <sub>O</sub> max	f = 1kHz, THD = 1%	3.0	4.0		Vp-p
Control voltage H level	V <sub>cnt</sub> H	Pin 7 input voltage	2.5		Vcc	V
Control voltage L level	V <sub>cnt</sub> L	Pin 7 input voltage	0		1.0	٧

### Design guarantee items

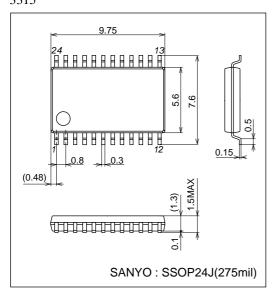
B	0 11	O - FG	Ratings			11.3
Parameter	Symbol	Conditions	min	typ	max	Unit
Video S/N	VG <sub>1V</sub>			-75	-70	dB
Differential Gain	DG	V <sub>IN</sub> = 1Vp-p, RAMP signal			1.0	%
Differential Phase	DP	V <sub>IN</sub> = 1Vp-p, RAMP signal			1.0	deg.
Mute attenuation	VMUTEV	V <sub>IN</sub> = 1Vp-p, f = 4.43MHz		-60	-55	dB
Cross-talk between	VCTKV	V <sub>IN</sub> = 1Vp-p, f = 4.43MHz		-60	-55	dB
channel						

#### **Truth Table**

	Pin 7	
Н	THROUTH	
L	MUTE	

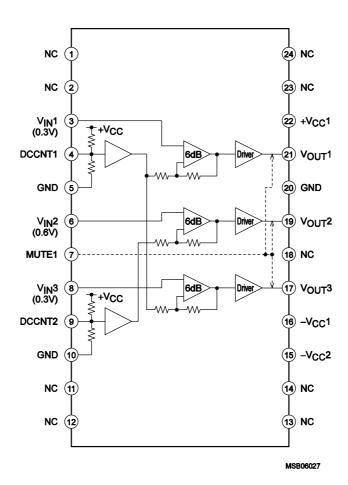
## **Package Dimensions**

unit : mm 3315

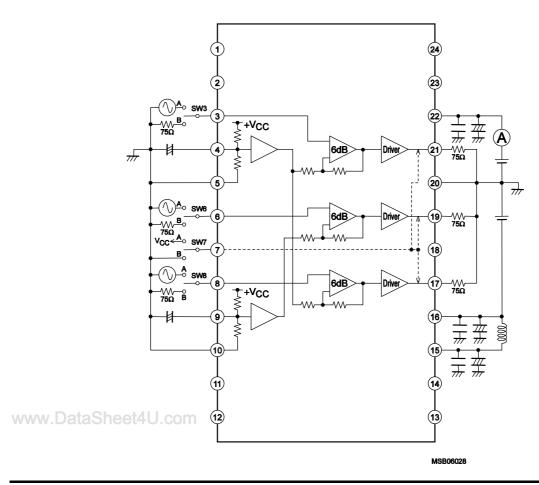


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#### **Block Diagram**



## **Test Circuit Diagram (Using ±power supply)**



## **Pin Functions**

	ICTIONS	I	T
Pin No.	Pin Name	Terminal Explanation	Equivalent Circuit
1	NC		
2			
11			
12			
13			
14			
18			
23			
24			
3	\/1	Input terminal.	
	V <sub>IN</sub> 1	1 -	
6	V <sub>IN</sub> 2	Non-bias. It is possible to use with being directly connected with	+VCC
8	V <sub>IN</sub> 3	DC. When DC coupling, it is necessary to add bias after the	1 A
		coupling.	9pF
			<b> </b>
			3 6 - W - W - Θ 8 1.2kΩ 1.6kΩ
			<b>6 → / / / / / / / / / /</b>
			$  \qquad (8) \qquad   \qquad 1.2k\Omega \qquad 1.6k\Omega \qquad   \qquad   \qquad  $
			<u> </u>
			-Vcc -Vcc
			MSP06253
4	DCCNT1	DC offset mode charge terminal between input and output.	
9	DCCNT2	When a condenser is input at the position between pin 4	
		(DCCNT1) and GND, the operation of IC becomes the mode	+VCC +VCC
		with 0.3VDC offset between input and output of	$\uparrow$ $\uparrow$ $\downarrow$
		1, 3ch (pins 3 and 21, pins 8 and 17).	
		Similarly when a condenser is input at the position between pin	
		9 (DCCNT2) and GND, it becomes the mode with 0.6VDC offset	
		between input and output of 2ch (pins 6 and 19).	4 + +
		l " " "	
		And when pins 4, 9 and GND is shorted, it becomes the mode	<b>↑ ♦</b> ↓
		without DC offset between input and output.	
			-V <sub>CC</sub> pin5,10
			MSP06254
	01:-		
5	GND	Both ±power supply and +power supply are GND.	
10			
20			
7	MUTE1	Changeover terminal of Mute.	
		When the Mute terminal is Low, it is Mute.	+Vcc
		When the terminal is Open, it is Low.	1
			Olco -
			9kΩ
			★ 😽     🛨
			4
			\ \footnote{\begin{array}{cccccccccccccccccccccccccccccccccccc
			-Vcc 7//-
			MSP06255

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Pin No.	Pin Name	Terminal Explanation	Equivalent Circuit
15 16	-VCC	-V <sub>CC</sub> of using ±power supply. Using +power supply, it is GND.	
17 19 21	VOUT3 VOUT2 VOUT1	Output terminal.  Using ±power supply, in case of the mode with DC offset, it is possible to use without capacitor of output by setting pins 3, 8 to 0.3V-bias and by setting pin 6 to 0.6V-vias. And in case of the mode without DC offset, it is possible to use without capacitor of output by setting each input to zero-bias. When using +power supply, both of the modes needs coupling capacitor.	+VCC +VCC (7) (9) (21) -VCC MSP06256
22	+VCC	Both ±power supply and +power supply are +V <sub>CC</sub> .	

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